

ATTORNEY DOCKET NO  
1067 – CA – C1



PATENT  
U.S. 10/715,950

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: John L. Melanson  
Serial No.: 10/715,950  
Filed: November 11, 2003  
Title: CIRCUITS AND METHODS FOR REDUCING INTERFERENCE FROM  
SWITCHED MODE CIRCUITS  
Confirm No.: 4450  
Art Unit: 2683  
Examiner: Stephen M. D'Agosta

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sirs:

**RESPONSE TO OFFICE ACTION**

In response to the Official Action mailed December 7, 2004, Applicant respectfully requests that the Examiner reconsider the rejection of the claims in view of the remarks set forth below.

Remarks/ Arguments begin on Page 2 of this paper